



#10  
3/28/03  
Model

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 10/050,334  
Filing Date ..... January 15, 2002  
Inventor ..... Vishnu K. Agarwal  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2813  
Examiner ..... Unassigned  
Attorney's Docket No. .... MI22-1913  
Title: Capacitor Constructions with Enhanced Surface Area

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References –See Attached Form PTO-1449

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449, copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a divisional application of co-pending application Serial No. 09/653,156, filed August 31, 2000. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. § 1.98(d) and MPEP § 609(2).

Citation of the referenced art is respectfully requested.

Respectfully submitted,

Dated: 19 Mar 2003

By: J.E. Lake  
James E. Lake  
Reg. No. 44,854

EV085426471